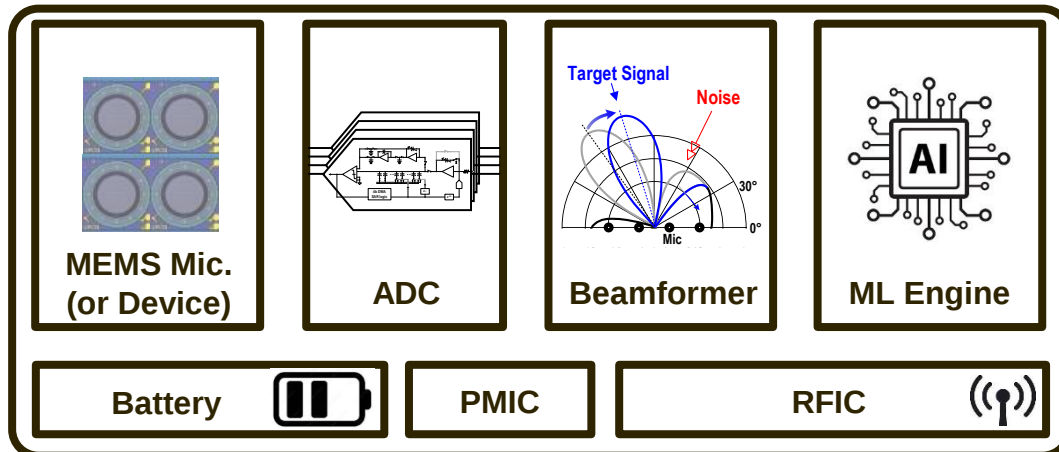


A portrait of Dr. J. H. Park, a man with short dark hair, smiling. He is wearing a dark blue sweater. The background is a blurred outdoor setting with trees and a building.

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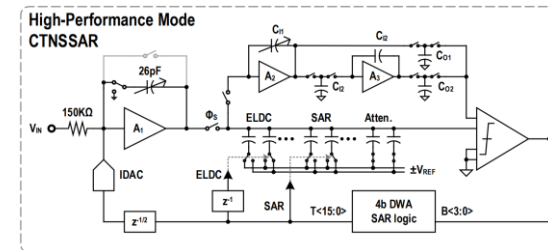
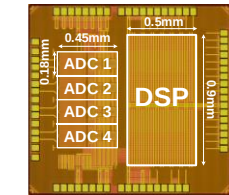
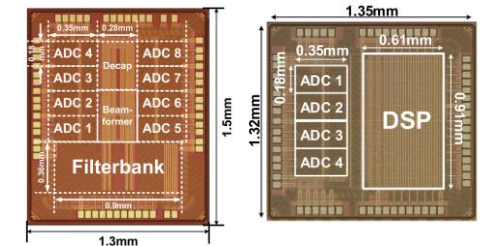
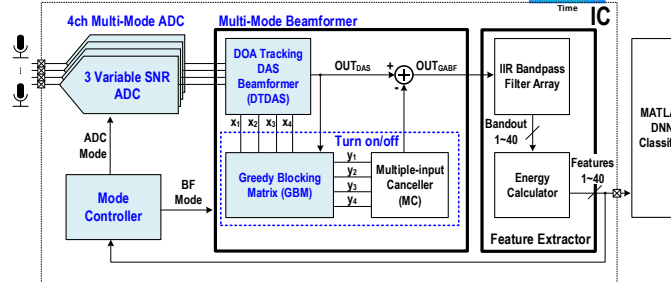
- B.S. Seoul National University, 2011
- M.S. Seoul National University, 2013
- Circuit Designer, Silicon Mitus, Korea, 2013-2016
- Ph.D. University of Michigan, 2021
- Circuit Designer, Apple, CA, USA, 2021~2024
- Assistant Professor in SKKU, 2024~current

Analog and Digital Circuit System Design



Goal: Implement Complete Sensor System IC

The diagram illustrates a deep learning architecture for speech processing. It starts with multiple microphones (MIC) receiving signals from a person and a car (Noise). The signals are converted by ADCs and processed by an Acoustic Beamformer DSP. The output is a 'Noisy Signal' waveform, which is then processed by 'Noise Suppression' to produce a cleaner waveform. The processed signal is then passed through a 'Feature Extractor' and a 'Classifier'.



- Beamforming algorithm
- DSP design
- High-performance ADC design
- Machine learning engine

[illegible]